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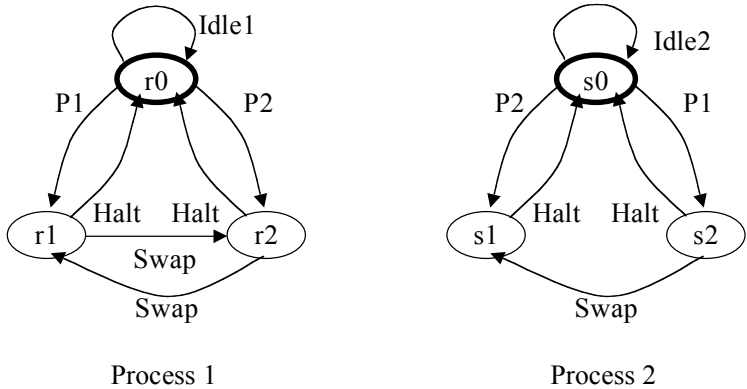
Leopold-Franzens Universität Innsbruck
 Fakultät für Informatik
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SS 2005
 Final Exam
 06. July 2005

Final Exam “ Specification of Distributed Systems”

Question 1 Labeled Transition System (9 credits)

The LTS for Process 1 and Process 2, resp., each describe a process participating in a leader selection protocol. Action “P1” selects Process 1, “P2” selects Process 2, “Halt” nullifies the election, “Swap” swaps leadership between the processes. Process 1 has alphabet { Idle1, P1, P2, Halt, Swap}, Process 2 has alphabet { Idle2, P1, P2, Halt, Swap }. r0 and s0, resp., are the initial states of Process 1 and Process2, resp.



- i. (3 credits) Mark each of the following statements as either true (“T”) or false (“F”):
 - a) T F The sequence “ Idle1 Idle2 P1 Swap Swap” is an observation trace of the synchronized product transition system of Process 1 and Process 2
 False; ”Idle1 Idle2 P1 Swap Swap” is not a possible observation trace of the product automaton, since “Idle2 P1 Swap Swap” is not a trace of Process 2.
 - b) T F The causal order of each event structure describing an execution of the synchronized product transition system is linear.
 False; Each event structure containing concurrent actions Idle1 and Idle2 is not linear.
 - c) T F The synchronized product transition system is deterministic.
 True, since the transition systems of each process are deterministic.

- ii. (6 credits) Define the synchronized product transition system (S,A,S₀,T) of Process 1 and Process 2
 LTS = (S,A,S₀,T) with
 - S = { r0s0, r1s2,r2s1 } (1 credit)
 - A = { Idle1, Idle2, P1, P2, Hold, Swap} (1 credit)
 - S₀ = { r0s0 } (1 credit)

- $T = \{ (r0s0, Idle1, r0s0), (r0s0, Idle2, r0s0), (r0s0, P1, r1s2), (r0s0, P2, r2s1), (r1s2, Hold, r0s0), (r2s1, Hold, r0s0), (r1s2, Swap, r2s1) \}$ (3 credits with 1 credit for each class of transition, -1 credits for transition $(r2s1, Swap, r1s2)$, 0.5 credits for any other missing transition per class)

Question 2 Data Flow Systems (4 credits)

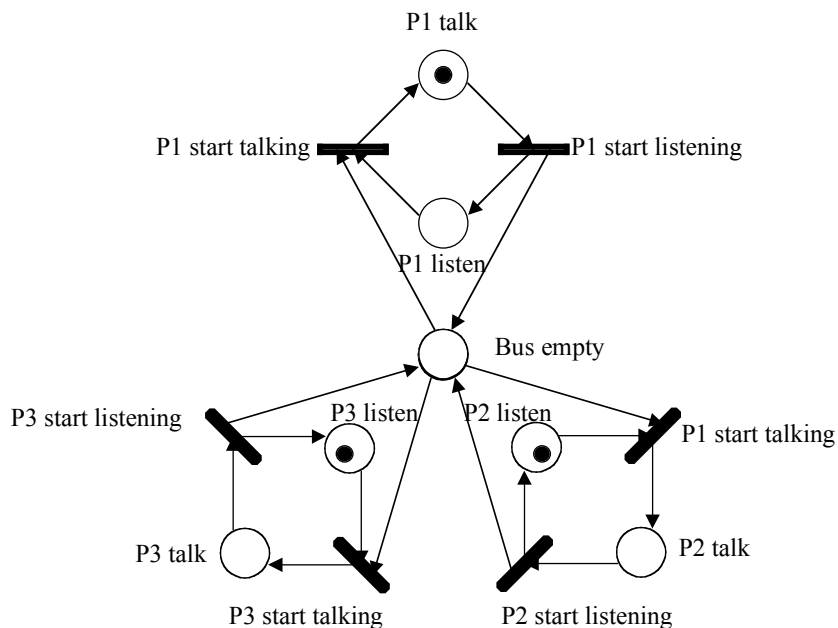
A demux component takes an integer value on its input channel, and distributes it via one of its two output channels. Using untimed structured behavior, it can be described as the largest set $DEMUX \subseteq \mathbb{N} \times \mathbb{N} \times \mathbb{N}$ with

$$(x, y, z) \in DEMUX \Leftrightarrow (x = \infty \wedge y = \infty \wedge z = \infty) \vee (x = a \bullet r \wedge y = a \bullet s \wedge z = t \wedge (r, s, t) \in DEMUX) \vee (x = a \bullet r \wedge y = s \wedge z = a \bullet t \wedge (r, s, t) \in DEMUX)$$

- i. (4 credits) Mark each of the following statements as either true (“T”) or false (“F”):
- T F $(1 \bullet 2 \bullet 3, 1 \bullet 3, 2) \in DEMUX$ and $(1 \bullet 2 \bullet 3, 1, 2 \bullet 3) \in DEMUX$
True
 - T F DEMUX is deterministic
False, e.g. $(1, 1, \infty)$ and $(1, \infty, 1) \in DEMUX$
 - T F DEMUX is input-enabled
True.
 - T F DEMUX can be equivalently described by a Kahn function
False, since non-deterministic behavior can only be equivalently be described by a set of Kahn functions

Question 3 Petri Nets (7 credits)

Consider the following Condition Event Net describing three components broad-casting over a common bus.



- i. (3 credits) Mark each of the following statements as either true (“T”) or false (“F”).

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- a) T F The number of marks is constant throughout the execution of the net
 False, since the configuration obtained by firing transition “P1 start listening” leads to 4 marks.
- b) T F The marking {P1talk, P2talk, P3talk} is reachable from the initial marking.
 False, since the marking is not reachable since all transitions “PX start talking” are in conflict.
- c) T F The marking {P1 listen, P2 listen, P3 listen} is reachable from the initial marking.
 The marking is not reachable since it must include the “Bus empty” mark.
- ii. (2 credits) What is the smallest set of places including P1talk, P2talk and P3talk forming a place invariant cornering all transitions?
 The set must additionally include “Bus empty” (1 credit)
 The set may not contain any other location (1 credit)
- iii. (1 credit) What is the number of marks in this set?
 The number of marks is 1. (1 credit)
- iv. (1 credits) Give an interpretation of the invariant concerning the collision of talking processes.
 At most one process is talking: either exactly one process is talking or the bus is empty.

Question 4 Process Algebra (9 credits)

The following process terms with alphabet $A = \{PIN, open, close, lock, unlock\}$ specify the lock/unlock functions of a digital vault:

- $S1(P) = PIN \rightarrow unlock \rightarrow open \rightarrow P, S2(P) = PIN \rightarrow lock \rightarrow close \rightarrow P$
- $V = \mu P. (S1(P) \mid S2(P))$
- $S(Q) = PIN \rightarrow ((unlock \rightarrow open \rightarrow Q) \mid (lock \rightarrow close \rightarrow Q))$
- $W = \mu Q. S(Q)$

The additional process term specifies a user never using the lock function of the vault.

- $T(R) = PIN \rightarrow unlock \rightarrow open \rightarrow R$
- $U = \mu R. T(R)$

- i. (3 credits) Mark each of the following statements as either true (“T”) or false (“F”):
- a) T F $PIN \cdot unlock \cdot open$ is a complete interace trace of $v \parallel u$
 False; it is a partial interaction trace, but not a complete interaction trace, since action P is possible before deadlock.
- b) T F The processes v and w have the same set of complete interaction traces.
 True, the processes do have the same set of complete interaction traces.
- c) T F $v \parallel u$ and $w \parallel u$ have the same set of complete interaction traces.

False; the combined processes do not have the same set of complete interaction traces, since PIN is a complete interaction trace of the former but not of the later.

- ii. (4 credits) Show $z = T(z)$ for $z = w \parallel u$ by algebraic reasoning, including the law $(P \mid Q) \mid (a \rightarrow R) = (P \mid a \rightarrow R) \mid (Q \mid a \rightarrow R)$.

First, we show that $z = R(z)$ via

$z =$ (via. Def. Z)

$w \parallel u =$ (via Def. W, U)

$(\mu Q. (S(Q)) \mid (\mu R. T(R))) =$ (via $\mu P. F(P) = F(\mu P. F(P))$)

$S(\mu Q. (S(Q)) \mid T(\mu R. T(R)))$ (via Def. W, U)

$S(W) \parallel T(U) =$ (via Def. S, T)

$(PIN \rightarrow (\text{unlock} \rightarrow \text{open} \rightarrow W) \mid (\text{lock} \rightarrow \text{close} \rightarrow W)) \mid (PIN \rightarrow \text{unlock} \rightarrow \text{open} \rightarrow U) =$ (via $(a \rightarrow P) \mid (a \rightarrow Q) = a \rightarrow P \mid Q$)

$PIN \rightarrow (((\text{unlock} \rightarrow \text{open} \rightarrow W) \mid (\text{lock} \rightarrow \text{close} \rightarrow W)) \mid (\text{unlock} \rightarrow \text{open} \rightarrow U)) =$ (via above law)

$PIN \rightarrow (((\text{unlock} \rightarrow \text{open} \rightarrow W) \mid (\text{unlock} \rightarrow \text{open} \rightarrow U)) \mid ((\text{lock} \rightarrow \text{close} \rightarrow W) \mid (\text{unlock} \rightarrow \text{open} \rightarrow U))) =$ (via $a \rightarrow P \mid b \rightarrow Q = a \rightarrow P \mid Q$ for $a \neq b$)

$PIN \rightarrow ((\text{unlock} \rightarrow \text{open} \rightarrow (W \mid U)) \mid STOP) =$ (via $P \mid STOP = P$)

$PIN \rightarrow \text{unlock} \rightarrow \text{open} \rightarrow (W \mid U) =$ (via Def. Z)

$PIN \rightarrow \text{unlock} \rightarrow \text{open} \rightarrow z =$ (via Def. T)

$T(z)$

- iii. (2 credits) Show $u = w \parallel u$ by algebraic reasoning using the result of ii.

$z = T(z) \Rightarrow$ (via law of induction)

$z = \mu R. T(R) \Rightarrow$ (via Def. Z)

$w \parallel u = \mu R. T(R) \Rightarrow$ (via Def. U)

$w \parallel u = u$

Question 5 Temporal Logics (11 credits)

To define properties about the leadership-election process in Question 2 the relevant propositions are „P1 is leader“, „P2 is leader“, „P1 is ready“, „P2 is ready“. A process is ready if it can react on selection actions „P1“ and „P2“.

- ii. (6 credits) Define a Kripke structure $K = (S, S_0, T, P, O)$

$K = (S, S_0, T, P, O)$ with

- $S = \{ r0s0, r1s2, r2s1 \}$ (0.5 credit)

- $S_0 = \{ r0s0 \}$ (0.5 credit)

- $T = \{ (r0s0, r0s0), (r0s0, r1s2), (r0s0, r2s1), (r1s2, r0s0), (r2s1, r0s0), (r1s2, r2s1) \}$ (2 credits with 0.5 credit for each class of transition, -0.5 credits for transition $(r2s1, r1s2)$)

- $P = \{ \text{“P1 is leader”}, \text{“P2 is leader”}, \text{“P1 is ready”}, \text{“P2 is ready”} \}$ (1 credit)

- $O = \{ (r0s0, \{ \text{“P1 is ready”}, \text{“P2 is ready”} \}), (r1s2, \text{“P1 is leader”}), (r2s1, \text{“P2 is leader”}) \}$ (2 credit)

- iii. (3 credits) Mark each of the following statements as either true (“T”) or false (“F”):

a) T F $(GF \text{ “P1 is ready”}) \Rightarrow (GF \text{ “P1 is leader”})$ holds in K

False. Since the path consisting of only of states $(r0s0)$ contradicts that: since $G \text{ “P1 is ready”}$ holds, but $G (\neg \text{“P1 is leader”})$

b) T F $AG \neg (\text{“P1 is leader”} \wedge \text{“P2 is leader”})$ holds in K

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True, since the observation for all states is either “P1 is ready” \wedge “P2 is ready”, “P1 is leader”, or “P2 is leader”.

- c) T F $A(GF \text{ “P1 is leader”}) \Rightarrow GF(\text{ “P2 is leader”} \vee \text{ “P1 is ready”})$ holds in K

True, since “P1 is leader” $\Rightarrow X(\text{ “P2 is leader”} \vee \text{ “P1 is ready”})$

- iv. (2 credits) Define the property “During some execution, both processes will always eventually become leader”.

$E(GF \text{ “P1 is leader”}) \wedge (GF \text{ “P2 is leader”})$ (1 credit for correct use of E, 1 credit for GF)